,	Application No.	plication No. Applicant(s)	
Notice of Allowability	10/814,063	MITA, KIYOSHI	
	Examiner	Art Unit	
	Asok K. Sarkar	2829	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT F of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in b) or other appropriate comm RIGHTS. This application is s	n this application. If not included unication will be mailed in due course	e. THIS le initiative
1. \boxtimes This communication is responsive to <u>the application filed</u>	July 26, 2004 .		
2. The allowed claim(s) is/are <u>1-5</u> .			
3. The drawings filed on are accepted by the Examin	er.		
 4. Acknowledgment is made of a claim for foreign priority of a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 1. Certified copies of the priority documents have 2. Certified copies of the certified copies of the priority documents have 1. Certified copies not received: 	re been received. re been received in Application	on No	om the
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirem	nents
5. A SUBSTITUTE OATH OR DECLARATION must be subminFORMAL PATENT APPLICATION (PTO-152) which gives			E OF
 CORRECTED DRAWINGS (as "replacement sheets") mu (a) ☐ including changes required by the Notice of Draftsper 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☒ including changes required by the attached Examiner Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 	rson's Patent Drawing Review r's Amendment / Comment o 1.84(c)) should be written on the header according to 37 Cl	r in the Office action of he drawings in the front (not the back) R 1.121(d).	
7. DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MAT FOR THE DEPOSIT OF BI	ERIAL must be submitted. Note the DLOGICAL MATERIAL.	ne
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB. Paper No./Mail Date	6. ☐ Interview S Paper No. 708), 7. ☑ Examiner's	formal Patent Application (PTO-152) ummary (PTO-413), /Mail Date Amendment/Comment Statement of Reasons for Allowance	

DETAILED ACTION

EXAMINER'S AMENDMENT

- 1. The Applicant's representative was contacted for permission to label Figs 12A and 12B as Prior Art.
- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Samuel Borodach on November 17, 2004.

The application has been amended as follows:

Figs 12A and 12B have been labeled with markings "Prior Art".

Allowable Subject Matter

- 3. Claims 1 5 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

Claims 1 – 5 recite, inter alia, a method of manufacturing a semiconductor device in which the terminal portions for the device are formed by etching a first conductive foil, laminating a second conductive foil to the first conductive foil through a resin layer and etching a conductive pattern on the second conductive foil, connecting the terminals and the conductive patterns, fixing a semiconductor chip to the two laminated foils and connecting the semiconductor element to the conductive pattern. Swada, US 5,424,250; Fukui, US 6,352,879; Takeuchi, US 6,475,629 and Sakamoto, US 6,562,660

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teach methods for manufacturing a semiconductor device by attaching a semiconductor die on a resin or adhesive sheet, they fail to teach the steps of forming the terminal portions for the device by etching a first conductive foil, laminating a second conductive foil to the first conductive foil through a resin layer and etching a conductive pattern on the second conductive foil, connecting the terminals and the conductive patterns to which the semiconductor element is electrically connected. Additionally, the art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Conclusion

- Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Asok K. Sarkar whose telephone number is 571 272 1970. The examiner can normally be reached on Monday Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Tokar can be reached on 571 272 1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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7. Information regarding the status of an application may be obtained from the

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Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Asok K. Sarkar

November 17, 2004

Patent Examiner